In the Specification:

The paragraph beginning on page 1, line 11 has been rewritten to read as follows:

--Fig. 30 shows an example of a configuration of an active matrix liquid crystal display. The liquid crystal panel has a structure in which two glass substrates, i.e., a TFT substrate 200 formed with TFTs (thin film transistors) and the like and a CF substrate 202 formed with a color filter (CF) and the like are in a face-to-face relationship with each other and are bonded together with liquid crystals 204 sealed therebetween.--

The paragraph beginning on page 2, line 14 has been rewritten to read as follows:

--Referring again to Fig. 30, a gate driving circuit 206 loaded with driver ICs for driving the plurality of gate bus lines 218 and a drain driving circuit 208 loaded with driver ICs for driving the plurality of drain bus lines 220 are provided on the TFT substrate 200 which is provided in a face-to-face relationship with the CF substrate 202 with the liquid crystals 204 sealed therebetween. Those driving circuits 206 and 208 output scan signals and data signals to predetermined gate bus lines 218 and drain bus lines 220 based on predetermined signals output by a control circuit 216. A polarizer 212 is provided on the surface of the TFT 200 opposite to the surface thereof on which the elements are formed, and a back-light unit 214 is attached to the surface of the polarizing plate 212 opposite to the TFT substrate 200. A polarizer 210 in a crossed Nicol relationship with the polarizer 212 is

B2 attached to the surface of the CF substrate 202 opposite to the surface thereof on which the color filter is formed.--

The paragraph beginning on page 8, line 27 has been rewritten to read as follows:

--The above-described object is achieved by a method for repairing defects of a display having pixel regions formed on a substrate, comprising the steps of irradiating a B3 multi-layer region formed by stacking a plurality of conductive layers with insulation layers interposed with a laser beam and selectively removing only an upper conductive layer in the vicinity of the multi-layer region such that neither inter-layer short-circuit nor short-circuit in a single layer occurs in the multi-layer region.--

The paragraph beginning on page 13, line 2 has been rewritten to read as follows:

--A description will now be made of a method for repairing a defect in a display according to a first mode for carrying out the invention with reference to Figs. 1a B4 through 7d. First, the method for repairing a defect according to the present mode for carrying out the invention will be schematically described with reference to Figs. 1a through 2. Components having the same functions and operations of those according to the related art shown in Figs. 30 through 33b will be indicated by like reference numbers and will not be described here.--